

Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application.

Listing of Claims:

1-10. (Canceled)

11. (Previously Presented) An image display having a plurality of thin film transistors and a plurality of capacitors on a substrate, wherein a plurality of gate-lines and a plurality of signal-lines which cross said plurality of gate-lines in a matrix shape are formed on said substrate, each of said thin film transistors has: an island-shaped semiconductor layer having a source region, a drain region, and a channel region sandwiched between them; a first insulation film formed between said island-shaped semiconductor layer and a gate electrode of the same layer as that of said gate-lines; an interlayer insulation film formed above said island-shaped semiconductor layer; and a source electrode and a drain electrode which are come into contact with said source region and said drain region via an opening formed in said interlayer insulation film and which exist in the same layer as that of the signal-lines, and each of said capacitors has: a storage electrode of the same layer as that of said gate-lines; a second insulation film formed in contact with said storage electrode, an upper surface of said interlayer insulation film, and a side surface of the

opening formed in said interlayer insulation film; and an electrode which is formed on said second insulation film so as to be in contact therewith and which exists in the same layer as that of said signal-lines.

12. (Original) A display according to claim 11, wherein relative permittivity of said second insulation film is higher than that of said first insulation film.

13. (Original) A display according to claim 11, wherein said second insulation film is an insulation film made of an organic material.

14. (Previously Presented) A manufacturing method of an image display, comprising the steps of:

forming a plurality of island-shaped semiconductor layers onto a substrate;
forming a first insulation film onto said island-shaped semiconductor layers;
forming a gate electrode and a storage electrode onto said first insulation film;
forming a source region, a drain region, and a channel region sandwiched between them onto said island-shaped semiconductor layers;
forming a second insulation film onto said storage electrode;
patterning said second insulation film, and etching only said second insulation film so as to be in contact only with an upper surface and side surfaces of said storage electrode and an upper surface of said first insulation film;

forming interlayer insulation films onto regions above said gate electrode and said storage electrode;

simultaneously removing said interlayer insulation film of a contact hole portion and said interlayer insulation film above said storage electrode; and

simultaneously forming an electrode on said second insulation film and a source electrode and a drain electrode which are connected to said source region and said drain region.

15. (Original) A method according to claim 14, wherein said second insulation film is formed to an upper portion and a side portion of said gate electrode simultaneously with said step of forming the second insulation film onto said storage electrode.

16. (Previously Presented) A display according to claim 11, wherein each of said capacitors is a parallelly-connected capacitor consisting of a first capacitor and a second capacitor,

 said first capacitor is formed by said island-shaped semiconductor layer, said first insulation film and said storage electrode, and

 said second capacitor is formed by said storage electrode, said second insulation film and an electrode which is formed on said second insulation film so as to be in contact therewith and which exists in the same layer as that of said signal-lines.

17. (Canceled)

18. (Previously Presented) A manufacturing method according to claim 14, wherein said first insulation film and said interlayer insulation films are made of silicon oxide.